Abstracts

Parallel computation for microwave circuit simulation

D.L. Rhodes and B.S. Perlman. "Parallel computation for microwave circuit simulation." 1997 Transactions on Microwave Theory and Techniques 45.5 (May 1997, Part I [T-MTT]): 587-592.

In this paper, the results of implementing a harmonic balance simulator, AGILE, on a variety of massively parallel computers (MPCs) is given. Descriptions of the computer hardware, which includes both shared-memory and message-passing implementations, and algorithms used to parallelize the computations are presented. The computers used include the CM-5, KSR-1, and a networked set of nonheterogeneous workstations using UNIX RPC. A key aspect is the description of the variety of algorithms used for each computer and the results obtained.

❑ Return to main document.